

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Group Art Unit: 2629
Jun KOYAMA et al.) Examiner: S. Kumar
Serial No. 10/827,438) CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 2, 2007. Molum Manupur Ma
Filed: April 20, 2004	
For: DRIVING CIRCUIT OF A	
SEMICONDUCTOR DISPLAY	
DEVICE AND THE	
SEMICONDUCTOR DISPLAY	
DEVICE)

RESPONSE

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

The Official Action mailed October 2, 2006 has been received and its contents carefully noted. This response is filed within three months of the mailing date of the Official Action and therefore is believed to be timely without extension of time. Accordingly, the Applicant respectfully submits that this response is being timely filed.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on April 20, 2004; May 3, 2004; June 30, 2004; November 11, 2004; January 25, 2005; February 22, 2005; May 25, 2005; and September 12, 2005.

Claims 1-46 are pending in the present application, of which claims 1-32 are independent. All claims stand rejected based on the judicially created doctrine of obviousness-type double patenting based on claims 1-42 of U.S. Patent 6,549,184 to Koyama et al. Reconsideration is requested.

As stated in MPEP § 804, under the heading "Obviousness-Type," in order to form an obviousness-type double patenting rejection, a claim in the present application

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